

Analog Design Contest

Report Submission Rules and Recommendations



Analog Design Contest

Bring ideas to life and get noticed



Rules:

- All Project Reports are limited to **10 pages maximum** (including title page)
- Title page, page 1 of 10, **MUST** be the same as our template: [See Template A.](#)
- Font: **Arial**
- Size: **12**
- Language: **English**
- Page Size: **A4**
- Spacing: **Single**
- Margins: **2007 Microsoft Word Default 'Normal' Margin Settings**
(Top 1", Bottom 1", Left 1", Right 1") [See Template B.](#)
- Accepted File Formats: **.docx** (Word Document), **.doc** (Word 97-2003) and **.pdf** (PDF)
- Must include a detailed written description of the design and a specific description of how each Texas Instruments analog IC or processor benefited the overall design.
- Must include a clear block diagram.
- Design Report must be submitted to adceurope@list.ti.com on **July 31st** at 6:00pm (CET).
- Design Report must be a maximum of 10MB.

Recommendations:

We recommend that you follow this structure for your design report:

- Title Page – ([Template A](#))
- Introduction
- Motivation For Project
- Theoretical Background
- Implementation
- Experimental Results
- Conclusions
- Summary

- Future Plans

TEMPLATE A

PROJECT TITLE: *Full Project Title*

TEAM LEADER: *First Name, Surname and E-mail Address*

TEAM MEMBERS: *First Name and Surname*

ADVISING PROFESSOR: *First Name, Surname and E-mail Address*

UNIVERSITY: *Full University Name, Country*

DATE: *Day/Month/Year*

TI PARTS USED IN PROJECT: *Quantity and TI Part Number*

PICTURE OF TEAM

REQUIRED

PICTURE OF PROJECT
HARDWARE SETUP

REQUIRED

PROJECT ABSTRACT (250 words maximum)

TEMPLATE B

Example of Page Layout

The ADCPro™ enables customers to evaluate the performance of a selection of data converters. It is an easy tool to test parameters such as Signal-to-Noise Ratio, Total Harmonic Distortion, Signal-to-Noise and Distortion, Spurious-Free Dynamic Range and Integral Nonlinearity using dynamic and static tests.

The THS1206M EVM allows the simultaneous sampling of 4 single-ended signals or 2 differential signals or a combination of both. This EVM can accommodate all eight devices of the THS1206 family of data converter, which gives the board the versatility to be used for multiple projects.

The ADS1258 EVM accepts combinations of eight differential or 16 single-ended inputs with a full-scale differential range of 5V or true bipolar range of $\pm 2.5\text{V}$ when operating with a 5V reference. The fourth-order delta-sigma modulator is followed by a fifth-order sinc digital filter optimized for low-noise performance. The PDK includes an ADS1258EVM, a USB-based motherboard for easy connection to the PC and the ADCPro™ software for evaluation.

The ADS8422EVM is a modular board which allows the users the ability to create custom analog signal-conditioning circuits, as well as different reference sources and interfaces. The analog input circuitry, consisting of three operational amplifiers allows the users the ability to change the passive components to customize the input. The EVM lets the user operate with an external reference voltage in a range up to 4.15.Volts. The internal reference is buffered out of the device by components on the EVM, relieving the user from supplying an external amplifier as a drive element.

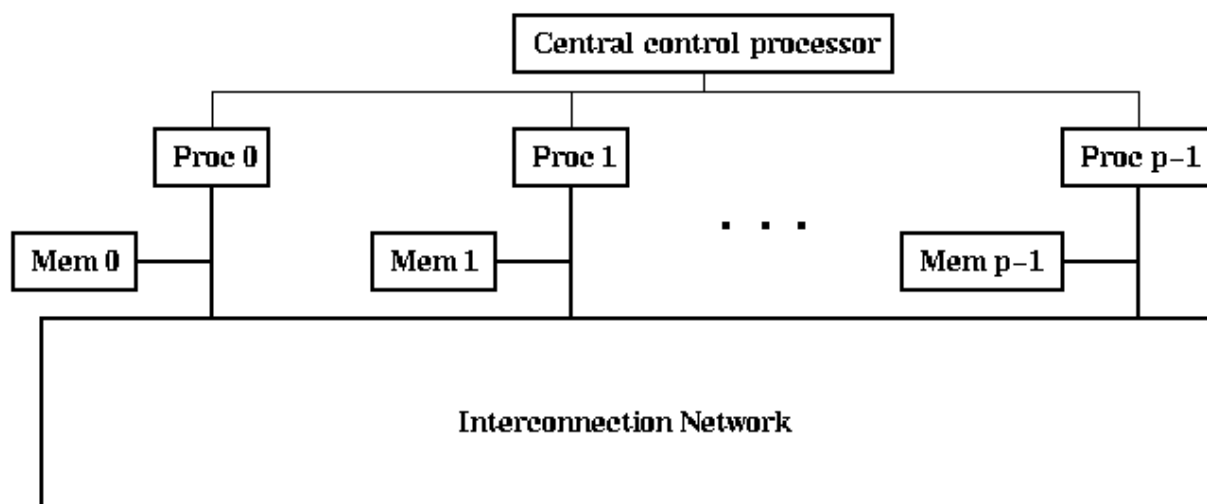


Figure 1: An example block diagram